

CLAIMS

1. An amplifier circuit for amplifying a downstream signal carrying voice over internet protocol data comprising:
- 5 an input which receives the downstream signal;
- an amplifier which receives the downstream signal from the input and amplifies the downstream signal, wherein the amplifier outputs an amplified signal carrying voice over internet protocol data;
- an output which receives the amplified signal from the amplifier, wherein the
- 10 output is connected with an external device capable of receiving and converting voice over internet protocol data to sound; and
- a bypass circuit connecting the input directly to the output, wherein upon activation of the bypass circuit, the downstream signal is transmitted directly from the input to the output and bypasses the amplifier.
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2. The amplifier circuit of claim 1, wherein the downstream signal is between 43 and 2000 MHz.
3. The amplifier circuit of claim 1, wherein the amplified signal is from 5
- 20 to 50 decibels greater than the downstream signal.
4. The amplifier circuit of claim 1 further comprising a first high-pass/low-pass filter connected between the input and the amplifier, wherein the first high-pass/low-pass filter only allows downstream signals to pass through to the amplifier.
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5. The amplifier circuit of claim 1 further comprising a second high-pass/low-pass filter connected between the output and the amplifier, wherein the second high-pass/low-pass filter only allows downstream signals to pass through to the external device capable of receiving and converting voice over internet protocol data to
- 30 sound.

6. The amplifier circuit of claim 1 further comprising a splitter located before the output for outputting the amplified signal to multiple external devices.

5 7. An amplifier for amplifying a signal comprising an amplifier circuit for amplifying a downstream signal carrying voice over internet protocol data, wherein the amplifier circuit comprises:

an amplifier connected between an input and an output of the amplifier circuit,
and

10 a bypass circuit connecting the input directly to the output, wherein upon activation of the bypass circuit, the downstream signal is transmitted directly from the input to the output and bypasses the amplifier.

8. The amplifier of claim 7, wherein the downstream signal is between 43
15 and 2000 MHz.

9. The amplifier of claim 7, wherein the amplifier circuit further comprises a first high-pass/low-pass filter connected between the input and the amplifier, wherein the first high-pass/low-pass filter only allows downstream signals to pass through to
20 the amplifier.

10. The amplifier of claim 7, wherein the amplifier circuit further comprises a second high-pass/low-pass filter connected between the output and the amplifier, wherein the second high-pass/low-pass filter only allows downstream signals to pass
25 through to the output.

11. The amplifier of claim 7, wherein the amplifier circuit further comprises a splitter located before the output for outputting the amplified signal to multiple external devices.

12. The amplifier of claim 7 further comprising an input connector connected with the input and an output connector connected with the output.

13. The amplifier of claim 7 further comprising a power supply connected
5 with the amplifier.

14. An amplifier circuit for amplifying a downstream signal carrying voice over internet protocol data comprising:

an amplifier having an input and an output, wherein the amplifier receives and
10 amplifies the downstream signal; and

a bypass circuit having an input connected with the amplifier input and an output connected with the amplifier output, wherein upon activation of the bypass circuit, the downstream signal is transmitted directly from the input to the output of the bypass circuit and bypasses the amplifier.

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15. The amplifier circuit of claim 14, wherein the bypass circuit comprises a switch, wherein upon activation of the switch, the downstream signal is transmitted directly from the input to the output of the bypass circuit and bypasses the amplifier.

20 16. The amplifier circuit of claim 14, wherein the bypass circuit comprises one of a mechanical switch, a relay, a manual switch, and an electronic switch.

17. The amplifier circuit of claim 14 further comprising a splitter located
after the amplifier, the splitter having multiple outputs for outputting the amplified
25 signal to multiple external devices.

18. The amplifier circuit of claim 17, wherein the output of the bypass circuit is connected directly to an output of the splitter.

19. The amplifier circuit of claim 14 further comprising a first high-pass/low-pass filter connected before the amplifier, wherein the first high-pass/low-pass filter only allows downstream signals to pass through to the amplifier.

- 5 20. The amplifier circuit of claim 19, wherein the wherein the downstream signal is between 43 and 2000 MHz.